



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Toshiyuki Kouchi *et al.*

Serial No.: Not yet assigned

Group Art Unit: Not yet assigned

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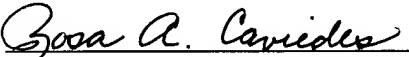
Examiner: Not yet assigned

Title: SEMICONDUCTOR INTEGRATED CIRCUIT

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Rosa A. Caviedes

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INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents
Washington, DC 20231

Dear Sir:

Pursuant to 37 CFR §1.56(a), Applicant hereby cites the following documents (copies enclosed) listed on the attached copy of Form PTO-1449.

The Commissioner is also authorized to charge any deficiencies in fees and credit any overpayment of fees to Deposit Account No. 07-1896.

Respectfully submitted,

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Sheet 1 of 1

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Form PTO-1449 (modified 2/91) INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)	U.S. DEPT. OF COMMERCE Patent and Trademark Office	Attorney Docket Number: 2102475-991110	Serial Number: Not yet assigned
	Applicant: Toshiyuki Kouchi et al.		
	Filing date: March 28, 2001	Group art unit: Not yet assigned	

U.S. PATENT DOCUMENTS

Examiner Initial	Patent number	Date	Name	Class	Sub-class	Filing date if appropriate

FOREIGN PATENT DOCUMENTS

Document number	Date	Country	Class	Sub-class	Translation	
					YES	NO
7-260884	Oct. 13, 1995	Japan				X

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner:	Date Considered:
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP '609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.	